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TITLE: Digital video coding apparatus, e.g. for  
teleconferencing

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INVENTOR-INFORMATION:

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ABSTRACT:

CHG DATE=19990617 STATUS=O>The coding apparatus has a vector processor (245) carrying out linear transformations on the video data. A bitstream processor compresses the vector processor output or decompresses an input bitstream for subsequent processing by the vector processor. A control circuit (248) synchronises the vector processing and the bitstream processing. One set of bitstream processing can be interrupted by the control circuit and a second set started, enabling two sets of digital video signals to be processes simultaneously. The two streams can be encoded or decoded in real-time.